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TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		9548.58USWO
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INTERNATIONAL APPLICATION NO.	INTERNATIONAL FILING DATE	PRIORITY DATE CLAIMED
PCT/CN00/00151	12 June 2000	25 November 1999

TITLE OF INVENTION

A METHOD AND A DEVICE FOR MULTI-SELECTION COHERENT DETECTION

APPLICANT(S) FOR DO/EO/US

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Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. [X] This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. [ ] This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. [X] This express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).
4. [ ] A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.

5. [X] A copy of the International Application as filed (35 U.S.C. 371(c)(2))
    a. [ ] is transmitted herewith (required only if not transmitted by the International Bureau).
    b. [X] has been transmitted by the International Bureau.
    c. [ ] is not required, as the application was filed in the United States Receiving Office (RO/US)
6. [X] A translation of the International Application into English (35 U.S.C. 371(c)(2)).

7. [X] Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))
    a. [ ] are transmitted herewith (required only if not transmitted by the International Bureau).
    b. [ ] have been transmitted by the International Bureau.
    c. [ ] have not been made; however, the time limit for making such amendments has NOT expired.
    d. [X] have not been made and will not be made.

8. [ ] A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).

9. [X] An unsigned oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)).

10. [ ] A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).

Items 11. to 16. below concern document(s) or information included:
11. [ ] An Information Disclosure Statement under 37 CFR 1.97 and 1.98.

12. [ ] An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.

13. [ ] A FIRST preliminary amendment.
    [ ] A SECOND of SUBSEQUENT preliminary amendment.

14. [ ] A substitute specification.

15. [ ] A change of power of attorney and/or address letter.

16. [X] Other items or information: Communication

U.S. APPLICATION NO (If known, see 37 C F R 1 5)	INTERNATIONAL APPLICATION NO	ATTORNEY'S DOCKET NUMBER
UNKNOWN **09/830385**	PCT/CN00/00151	9548.58USWO

17. [X] The following fees are submitted:	CALCULATIONS PTO USE ONLY

BASIC NATIONAL FEE (37 CFR 1.492(a) (1)-(5)):

Search Report has been prepared by the EPO or JPO...$860.00

International preliminary examination fee paid to USPTO
(37 CFR 1.492(a)(1))...$690.00

No international preliminary examination fee paid to USPTO (37 CFR 1.482)
but international search fee paid to USPTO (37 CFR 1.445(a)(2)).............................$710.00

Neither international preliminary examination fee (37 CFR 1.482) nor
international search fee (37 CFR 1.445(a)(3)) paid to USPTO $1000.00

International preliminary examination fee paid to USPTO (37 CFR 1.482)
and all claims satisfied provisions of PCT Article 33(2)-(4) $100.00

ENTER APPROPRIATE BASIC FEE AMOUNT =	$1000.00	

Surcharge of **$130.00** for furnishing the oath or declaration later than [ ] 20 [ ] 30 months from the earliest claimed priority date (37 CFR 1.492(e)).	$	

CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total claims	10 -20 =		X $18.00	$	
Independent claims	2 -3 =		X $80.00	$	
MULTIPLE DEPENDENT CLAIM(S) (if applicable)			+ $260.00	$	

TOTAL OF ABOVE CALCULATIONS =	$1000.00	

Reduction by 1/2 for filing by small entity, if applicable. Small entity status is claimed pursuant to 37 CFR 1.27	$	

SUBTOTAL =	$1000.00	

Processing fee of **$130.00** for furnishing the English translation later than [ ] 20 [ ] 30 months from the earliest claimed priority date (37 CFR 1.492(f). +	$	

TOTAL NATIONAL FEE =	$1000.00	

Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). $40.00 per property +	$	

TOTAL FEES ENCLOSED =	$1000.00	

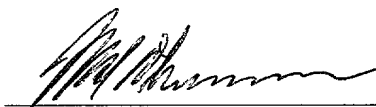|	Amount to be: refunded	$
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a.   [X] Check(s) in the amount of $1000.00 to cover the above fees is enclosed.

b.   [ ] Please charge my Deposit Account No. _____ in the amount of $ _____ to cover the above fees.
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c.   [X] The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any
        overpayment to Deposit Account No. 13-2725.

**NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR
1.137(a) or (b)) must be filed and granted to restore the application to pending status.**

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A METHOD AND A DEVICE
FOR MULTI-SELECTION COHERENT DETECTION

FIELD OF THE INVENTION

The present invention relates to a signal detection of the wireless mobile communication. More particularly, it relates to a method and its device for a multi-selection coherent detection.

BACKGROUND OF THE INVENTION

It is well known that the received signal may be affected by the so-called Doppler effect in the wireless mobile communication, and a Doppler frequency shift may be produced with respect to the transmitted signal; while the size and the feature of the said frequency shift may be determined by the relative motion between the transmitter and the receiver, the environment of the signal transmission, and etc. The phase rotation, even the phase reversion may be occurred on the phase of the received wireless communication transmitting signal, therefore, the influence of those factors should be fully considered while detecting the signal, otherwise, the performance of the signal detection will be affected inevitably.

The complex discrete signal transmitted is assumed as $X(k)$ $(k=0...L-1)$, then the received discrete signal after sampling will be:

$$Y(k)=a(k)*X(k)*e^{j\phi(k)}+n(k), (k=0...L-1) \qquad (1)$$

wherein, $a(k)$ is an attenuation factor after passing through the channel, $\phi(k)$ is a phase rotation angle after the signal passing through the channel, $n(k)$ is a additive noise, and k represents the indices of the discrete signal, under the condition of transmitting the data and sampling the signal at a constant rate, it can also be considered as a representation of the indices of the time. The total number of the signal samples for performing the signal detection is L.

Several typical signal detection methods of the related art are shown as below:

1. Coherent signal detection method, in which, the signal will be coherent accumulated directly in a range with a length of L. That is, the transmitted signal and the received signal will be multiplied by conjugation, and the multiplied result will be

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accumulated in the range with the length L. Then the summing result obtained finally is calculated for the square of the mode, and which is used as a decision statistic of the coherent detection. The decision statistic is represented by the following equation:

$$Z_{coherent} = \left| \sum_{k=0}^{L-1} Y(k) * X^*(k) \right|^2 \qquad (2)$$

The equation 2 will be substituted by the equation 1 of the received signal, obtaining:

$$Z_{coherent} = \left| \sum_{k=0}^{L-1} [a(k) * X(k) * X^*(k) * q^{j\phi(k)}] + n \right|^2 \qquad (3)$$

Where, $n = \sum_{k=0}^{L-1} X^*(k) * n(k)$, $X^*(k)$ is a complex conjugate signal of X(k).

A good detection result can only be obtained by using such detection method when the phase of the received signal varies not so significant within the length L, but it is difficult to achieve that the phase of the received signal varies not so significant within the length L under certain communication environment.

2. Non-coherent detection method: The fundamental idea of this detection method is to divide the signal of L samples used to detect the signal into the segments with equal space N_{noncoh} ($N_{noncoh} > 1$), the length of each segment is $L/N_{noncoh} = S_{noncoh}$, the coherent accumulating sum T(m) will be calculated for each length of S_{noncoh}, that is:

$$T(m) = \sum_{k=0}^{S_{noncoh}-1} [Y(m * S_{noncoh} + k) * X^*(m * S_{noncoh} + k)], \qquad m = 0 \ldots N_{noncoh} - 1 \qquad (4)$$

The coherent result of each segment is T(m), and there are total N_{noncoh} data, then the non-coherent accumulating will be performed again. The equation for calculating the decision statistic will be obtain as follow:

$$Z(k) = \sum_{m=0}^{N_{noncoh}-1} |T(m)|^2 \qquad (5)$$

Substitute the equation 1 and 4 into 5, then obtain:

$$Z(k) = \sum_{m=0}^{N_{noncoh}-1} \left| \sum_{k=0}^{S_{noncoh}-1} [a(m * S_{noncoh} + k) * X(m * S_{noncoh} + k) * X^*(m * S_{noncoh} + k) * e^{j\phi(m*S_{noncoh} + k)}] + n_m(m) \right|^2$$

(6)

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Where, $n_m(m) = \sum_{k=0}^{S_{noncoh}-1} [X^*(m * S_{noncoh} + k) * n(m * S_{noncoh} + k)]$.

It is required that the phase of the received signal remains constant within the signal length of S_{noncoh}, then a better performance can be achieved by this detection method. However, when the phase of the received signal may also be maintained constant within L ($L>S_{noncoh}$), then the detection performance loss of the non-coherent detection method will be smaller than the gain obtained from suppressing the phase rotation; in general, it is worse than the coherent detection method of Method 1.

3. Differential detection method: The fundamental idea of this method is also to divide L samples the signal used to detect the signal into segments with equal spaces N_{diff} ($N_{diff}>1$), the length of each segment is $L/N_{diff}=S_{diff}$, by using the coherent accumulating method for each length of S_{diff}, N_{diff} coherent accumulating values will be obtained:

$$Q(m) = \sum_{k=0}^{S_{diff}-1} [Y(m * S_{diff} + k) * X^*(m * S_{diff} + k)] \qquad m = 0 \ldots N_{diff} - 1 \qquad (7)$$

The coherent result $Q(m)$ of two consecutive segments will be conjugately multiplied each other, resulting in total of $N_{diff}-1$ multipliecations, then adding the real part of $N_{diff}-1$ multipliecations, and obtaining the following equation for calculating the decision statistic:

$$Z = \sum_{m=0}^{N_{diff}-2} \operatorname{Re}\{Q(m) * Q^*(m+1)\} \qquad (8)$$

Substitute the equation 1 and 7 into 8, and expend it to obtain:

$$z = \sum_{m=0}^{N_{diff}-2} \operatorname{Re}\{(\sum_{k=0}^{S_{diff}-1} [a(m*S_{diff}+k)*X(m*S_{diff}+k)*X^*(m*S_{diff}+k)*e^{j\phi(m*S_{diff}+k)}] + n_m(m))$$

$$*(\sum_{k=0}^{S_{diff}-1} [a^*((m+1)*S_{diff}+k)*X((m+1)*S_{diff}+k)*X^*((m+1)*S_{diff}+k)*e^{-j\phi((M+1)*S_{diff}+k)}] + n_n^*(m+1))$$

(9)

Where, $n_m(m) = \sum_{k=0}^{S_{diff}-1} [n(m * S_{diff} + k) * X^*(m * S_{diff} + k)]$

As same as Method 2, the application condition of the differential detection method can be satisfied only if that the phase of the received signal maintains substantially constant within S_{diff}. Nevertheless this method is also satisfied the

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application condition (the phase of the received signal maintains substantially constant within the length L), as compared with Method 1, the detection performance loss of the differential detection method itself is also smaller than the gain obtained from suppressing the phase rotation; in general, it is worse than the detection performance of the coherent detection method of Method 1.

4. Combination detection method: This method is the combination of the above three methods. The range of Doppler frequency shift may be detected (or specified) by certain methods. In the case of high Doppler frequency shift, the non-coherent or differential detection method may be used; while in the case of low Doppler frequency shift, the coherent detection method can be used. The implementation of this method is rather difficult, and the optimum detection performance can not be obtained yet.

By comparing in general:

Method 1 is established on the basis of assuming that the phase of the signal maintains substantially constant within a range of the length L, in the case of low frequency shift (including Doppler frequency shift and system frequency shift, the same below), this condition can be satisfied substantially (according to the chip rate and the signal length L, the same below), and an excellent detection effect can be reached; but in the case of large frequency shift, this assumption may not be established, within the length L, the signal phase may be change greatly even the phase reverse may be occurred, this may cause that the coherent results cancels out with each other, thereby the detection performance decreases rapidly, even non signal can be detected.

The application condition of Method 2 will relaxed to: remaining the signal phase within the length S_{noncoh} ($S_{noncoh}<L$), substantially constant, so that in the case of a higher frequency shift, the signal phase may not be changed greatly when it is within S_{noncoh} (it should be noted, that $S_{noncoh}=L/N_{noncoh}<L$), and the performance degradation appearing in Method 1 will no longer occur, then in this situation, the performance will be better than that of Method 1; however, in the case of a low frequency shift, Method 1 is also satisfied with the condition of maintaining substantially the signal phase as a constant within the coherent length L, at this time, comparing the non-coherent of Method 2 with Method 1, a certain performance loss may be existed.

Method 3 is as same as that analyzed in Method 2, in the case of higher frequency shift, though the differential detection method can obtain a better detection

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performance than that of Method 1; but in the case of low frequency shift, the performance loss of which will be large than Method 1 to a certain extent.

Although a better performance can be achieved by using Method 4 in the case of a variety of Doppler frequency shift, but it is complicated by using the method of the protocol limitation. By using the detection method, the Doppler frequency shift estimate module is required, so that, it is difficult to ensure the accuracy.

SUMMARY OF THE INVENTION

The object of the present invention addressed the problems existing in several detection methods listed above, and to provide a method and a device for multi-selection coherent detection, in order to reduce the influence of the phase rotation caused by the frequency shift, and to improve the signal detection probability.

In order to realize the above object, the multi –selection coherent detection method of the present invention is proposed, comprising the steps of:

A. The length L used for the signal detection may be divided into $N_{multicoh}$ segments, the coherent accumulating will be performed within each segment, total $N_{multicoh}$ coherent results can be obtained, they will be denoted as X_i ($i=0...N_{noncoh}-1$);

B. Various possible phase adjustment can be performed on those $N_{multicoh}$ coherent results (the number of the possible phase adjustments will be denoted as P) respectively, and the adjustment results will be denoted as $Y_{i,j}$ ($i=0...N_{multicoh}-1$, $j=0...P-1$);

C. A value of the adjustment result will be selected among P adjustment results corresponding to each coherent result and be combined with each other in various possible ways, the largest number of the combination will be $C=P^{N_{multicoh}}$;

D. $N_{multicoh}$ adjustment results in each combination will be coherent accumulated, and $C=P^{N_{multicoh}}$ coherent results can be obtained, and will be denoted as Z_t ($t=0...C-1$);

E. Among $C=P^{N_{multicoh}}$ coherent results, the optimum ones are selected as the detection results.

The multi-selection coherent detection device according to the present invention comprising:

a matched filter unit; two or more branch units; and a branch selection unit. The input signal will be input to the matched filter unit for carrying out matched and filtering; the output of the matched filter unit will be sent to each branch unit

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respectively; the phase adjustment and the coherent accumulation of the signal will be performed in each branch unit respectively, and then sent to the branch selection unit; the branch output with largest mode is selected to output by the branch selection unit.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a principle block diagram of a reverse access receiver system of the wide-band code division multiple access (WCDMA) according to the method of the present invention.

Fig. 2 is an illustration construction view showing a reverse access prefix multi-selection coherent detection device of the wide-band code division multiple access (WCDMA).

Fig. 3 is a diagram showing the comparison of the performance with respect to a reverse accessing prefix detection method of the wide-band code division multiple access (WCDMA).

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The coherent detection method according to the present invention comprises following steps: the length L used for the signal detection is divided into $N_{multicoh}$ segments of multi coherent segments, the coherent accumulating will be performed within each segment, then total $N_{multicoh}$ coherent results can be obtained, they will be denoted as X_i ($i=0...N_{noncoh}-1$); assuming that the signal is divided into segments with equal spaces, then the length of each segment will be $S_{multicoh}=L/N_{multicoh}$:

$$X_m = \sum_{k=0}^{S_{multicoh}-1} (Y(m * S_{multicoh} + k) * X * (m * S_{multicoh} + k)), \qquad m = 0...N_{multicoh} - 1$$

(10)

And then, various possible phase adjustment may be performed on those $N_{multicoh}$ coherent results respectively (the number of the possible phase adjustment will be denoted as P), and the adjustment results will be denoted as $Y_{i,j}$ ($i=0...N_{multicoh}-1$, $j=0...P-1$); a value of the adjustment result will be selected from the adjustment results to perform various possible combination; then those possible combination will be coherent accumulated again, after coherent accumulating, $C=P^{N_{multicoh}}$ coherent results can be obtained, and will be denoted as Z_t ($t=0...C-1$);

$$Z_t = \sum_{m=0}^{N_{multicoh}-1} [X_\tau * e^{j\phi_{t,m}}], t = 0 \ldots C-1 \qquad (11)$$

Among $C=P^{Nmulticoh}$ coherent results, the optimum ones are selected as the detection results. The method of the present invention follows the decision formula below:

$$Z = OPT\{Z_t\}, t = 0 \ldots C-1 \qquad (12)$$

Where, $OPT\{\}$ is a selected optimum operator, it means that the optimum value is selected from a series of values.

In general, the above segment dividing method is used for the segments with equal spaces, but it could be the segments with unequal spaces, according to the particular situation.

In the phase adjustment of step b, when the number of the phase adjustment is P, a phase rotation of $\phi = \phi 0 + k*2\pi/p$, $(k=0 \ldots P-1)$, will be performed on the signals respectively, wherein, $\phi 0$ can be any value. The relation between the phase rotation ϕ and p is not limited by this expression.

In step a, a coherent result X_i will be obtained for each segment, and the total number is $N_{multicoh}$, and P phase adjustment will be performed on each coherent result according to step b.

An adjustment coherent result will be selected from P adjustment coherent results corresponding to each above coherent result, then total $N_{multicoh}$ adjustment coherent results will be coherent added, and a final coherent result Z_t will be obtained. In this way, there are total $C=P^{Nmulticoh}$ selections, and again, $C=P^{Nmulticoh}$ final coherent results Z_t can be obtained.

In step c, the method of the largest mode may be used as a criterion of selecting the optimum ones.

The number of the said maximum coherent results is $C=P^{Nmulticoh}$, but it does not signify that $C=P^{Nmulticoh}$ coherent results must be obtained in the practical application; according to the situation, the method in which the number of the coherent results is less than $C=P^{Nmulticoh}$ may be used to reduce the number of the coherent results required.

Next, the prefix detection of the reverse access channel of the wide-band code division multiple access (WCDMA) used as an implementation of the method of the present invention will be described. The length of the prefix chip is 4096 chips, at the chip rate of 3.84 Mbp/s, and the carrier frequency of 2 GHz, and under the speed of 500 Km/h which is the requirement of the WCDMA protocol standard, the signal phase reverse may be occurred quite in a range of 4096 chips. If the above Method 1 is used under this condition, then the rapid degrading of the performance will be occurred inevitably (see curve 1 of Fig. 3).

Referring to Fig. 1, a system shown in Fig. 1 is the practical application of the method of the present invention. In the said system, the received signal reaches the band-pass filter 41 according to the wide-band code division multiple access (WCDMA) frequency band via an antenna 40, and a band limited signal will be obtained, and then reaches the base-band section 42. The conversion of the radio frequency signal to base-band signal, as well as the function of the digital to analog conversion will be performed in the base-band section 42, and a base-band signal can be obtained.

Alternatively, the base-band signal is sent to the multi-selection coherent circuit 43, and a multi-selection coherent signal will be obtained. The multi-selection coherent signal is sent to the multi-path retrieval circuit 44 to perform multi-path retrieval, the obtained multi-path information will be sent to the de-spread and RAKE combiner circuit 45 as a signal of its first input port.

On the other hand, the base-band signal will be sent directly to de-spread and RAKE combiner circuit 45 as a signal of its second input port. The base-band signal will be carried out de-spreading and RAKE combining by the de-spread and RAKE combiner circuit 45 on the basis of the multi-path information sent from the multi-path retrieval circuit 44, and the result will be output after combining.

Take $N_{multicoh}=4$, $P=2$, then there are $2^4/2=8$ kinds of the possible coherent results (because the largest mode method is utilized finally, so it is not necessary to carry out the phase adjustment for the data of the first segment, thereby, the number of the possible coherent results required can be reduced to one half); $S_{multicoh}=4096/4=1024$ chips, also, $P=2$ kinds of the phases are taken as 0° and 180°, then the corresponding phase adjustment can be indicated by multiplying by 1 or −1.

According to the above method of the present invention, the multi-selection

coherent detection device comprises a matched filter unit; two or more branch units; and a branch selection unit. The input signal will be input to the matched filter unit for carrying out matched and filtering; the output of the matched filter unit will be sent to each branch unit respectively; the phase adjustment and the coherent accumulation of the signal will be performed in each branch unit, and then sent to the branch selection unit; the branch output with the largest mode is selected to output by the branch selection unit.

Each said branch unit further comprises: a multiplier, for carrying out the phase adjustment; an adder, for carrying out the coherent accumulation; a holder, for holding the data; and a delay unit, for delaying the data; the output of the matched filter will be sent to the branch selection unit via the multiplier, the adder in turn; meanwhile, the adjustment series will be sent to the multiplier via the holder, and the output of the adder will be feedback to its input via the delay unit.

The operating principle and the operating process of the said device will be described in detail as follows:

As shown in Fig. 2, after the input signal being output through the matched filter, it is divided into eight branches, and sent to the multiplier 2~9 respectively as the signals of their first input ports. The construction of eight branch units is identical, except that the input signal of the holder 10~17 are different. Take branch unit 1 as an example, wherein a multiplier 2, a holder 10, an adder 18, and a delay unit 26 are included. The adjustment series of four bits 1, 1, 1, 1 will be inputted to the holder 10, and the information bits of each adjustment series will be held by the holder for a time period of 1024 chips, that is, the information of the first bit of the adjustment series may be outputted in the first 1024 chips time period; then the information of the second bit of the adjustment series may be outputted in the second 1024 chips time period; the information of the third bit of the adjustment series may be outputted in the third 1024 chips time period; and the information of the fourth bit of the adjustment series may be outputted in the fourth 1024 chips time period. The output of the holder 10 may be used as a signal for the second input port of the multiplier 2. In this way, 0° or 180° phase adjustment of the output signal of the matched filter 1 can be performed in the multiplier 2, and the signal after the phase adjustment will be sent to the adder 18 as a signal for its first input port, another input port of the adder 18 may be connected with the delay unit 26, the first input port and the second input port are

added by the adder, then it will be outputted to the selector 33 and the delay unit 26. The signal sent from the adder 18 is delayed in the delay unit for a time period of 1024 chips, and it will be feedback to the second input port of the adder 18. The adjustment series inputted to the holder 11 are 1, 1, 1, -1, and that inputted to the holder 12 are 1, 1, -1, 1, and that inputted to the holder 13 are 1, 1, -1, -1, according to this rule, that inputted to the holder 17 are 1, -1, -1, -1; and there are total eight holders and eight different input adjustment series.

The sum results of the adders 26~32 will be sent to the selector 33 respectively as the signals for its first to eighth input ports. The largest mode $Max_n s$ ($n=1,2...8$) will be calculated by the selector 33 for the input of each branch at the fourth segment of 1024 chips; by comparing these largest mode $Max_n s$, a maximum value Max can be obtained, the coherent result of the branch (denoted as 1) corresponding to the maximum value Max will be outputted as the final output of resulting detection.

Simulating tests of four detection methods, such as the coherent detection, non-coherent detection, differential detection and the multi-selection coherent detection of the present invention, have been carried out in the speed range of 5 km/h to 500 km/h, the simulating conditions are:

Constant False Alarm Rate (CFAR): 0.001

System frequency shift: 0 Hz

Channel type: Vehicle channel

ASNR: 5 dB

The results of the simulating tests are shown in Fig. 3, in which, the horizontal axis indicates the moving speed (km/hour), and the vertical axis indicates the detection probability. The curve of the differential detection is labeled as 2, and the curve of the non-coherent detection is labeled as 3. Obviously, the signal detection performance of the multi —selection coherent detection method (see curve marked 4) of the present invention is superior to that of the other three signal detection algorithms.

APPLICABILITY OF THE INDUSTRY

The length L used by the signal detection according to the present invention is divided into $N_{multicoh}$ segments, the coherent accumulating is carried out within each segment, then the phase adjustment will be performed for those $N_{multicoh}$ coherent results, and a value will be selected from the adjustment results corresponding to each

coherent result for carrying out various possible combinations; then these possible combinations will be accumulated coherently again, finally, the optimum ones will be selected as the detection results; and a coherent detection device is used according to the above method, the said device comprises a matched filter unit; two or more branch units; and a branch selection unit. The input signal will be inputted to the matched filter unit; the output of the matched filter unit will be sent to each branch unit respectively; it will be outputted after the branch circuit with the largest mode is selected by the branch selection unit.

The disadvantages, which exist in the above four methods, of the poor detection performance caused by the system frequency shift, the phase rotation, and etc. are overcome by the method and the device of the present invention. The influence of the frequency shift and the phase rotation that reduces the signal detection performance can be suppressed by the said method and the device to a certain extent, and the signal detection performance and the probability are improved.

What is claimed is:

1. A multi-selection coherent detection method, comprising the steps of:

A. dividing the length L used for the signal detection into $N_{multicoh}$ segments, performing the coherent accumulating within each segment, and obtaining total $N_{multicoh}$ coherent results denoted as X_i ($i=0...N_{noncoh}-1$);

B. performing various possible phase adjustments on those $N_{multicoh}$ coherent results (the number of the possible phase adjustments bing denoted as P), and denoting the adjustment results as $Y_{i,j}$ ($i=0...N_{multicoh}-1, j=0...P-1$);

C. selecting a value of the adjustment result from P adjustment results corresponding to each coherent result, and the largest number of the combinations being $C=P^{Nmulticoh}$;

D. coherently accumulating $N_{multicoh}$ adjustment results in each combination and obtaining $C=P^{Nmulticoh}$ coherent results denoted as Z_t ($t=0...C-1$);

E. among $C=P^{Nmulticoh}$ coherent results, selecting the optimum ones as the detection results.

2. A multi-selection coherent detection method according to Claim 1, wherein, the segments in step a are equal spaced or unequal spaced.

3. A multi-selection coherent detection method according to Claim 1, wherein, in the phase adjustment of step b, when the number of the phase adjustments is P, performing the phase rotation of $\phi = \phi_0 + k*2\pi/p$, ($k=0...P-1$), on the signals respectively, wherein, ϕ_0 may be any value.

4. A multi-selection coherent detection method according to Claim 1, wherein, in the said step a. obtaining one X_i for each segment, and there being total $N_{multicoh}$ coherent results; according to step b further, performing P phase adjustments for each coherent result, and obtaining total $N_{multicoh}*P$ adjustment coherent results.

5. A multi-selection coherent detection method according to Claim 4, wherein, selecting one adjustment coherent result from P adjustment coherent results corresponding to each coherent result, and carring out the coherent overlapping on total $N_{multicoh}$ adjustment coherent results, and obtaining a final coherent result Z_t obtained; in this way, there being total $C=P^{Nmulticoh}$ possible selection methods, then obtaining $C=P^{Nmulticoh}$ final coherent results Z_t further.

6. A multi-selection coherent detection method according to Claim 1, wherein, in step e, the method of the largest mode is used as a criterion for selecting the optimum ones.

7. A multi-selection coherent detection method according to Claim 5, wherein, the number of the largest coherent results is $C=P^{Nmulncoh}$, however it does not mean that $C=P^{Nmulticoh}$ coherent results must be obtained in the practical application; the number of the coherent results that less than $C=P^{Nmulticoh}$ may be used according to the situations to reduce the number of the coherent results required.

8. A multi-selection coherent detection device, wherein, the said detection device comprises: a matched filter unit; two or more branch units; and a branch selection unit. The input signal is input to the matched filter unit for carrying out matched and filtering; the output of the matched filter unit is sent to each branch unit respectively; the phase adjustment and the coherent accumulation of the signal is performed in each branch unit, and then sent to the branch selection unit; the branch output of selecting the largest mode is performed by the branch selection unit.

9. A multi-selection coherent detection device according to Claim 8, wherein, each said branch unit further comprises: a multiplier, for carrying out the phase adjustment; an adder, for carrying out the coherent accumulation; a holder, for holding the data; a delay unit, for delaying the data; the output of the matched filter is sent to the branch selection unit via the multiplier, and the adder in turn; meanwhile, the adjustment series is sent to the multiplier via the holder, and the output of the adder is feedback to its input via the delay unit.

10. A multi-selection coherent detection device according to Claim 9, wherein, both the holding time of the holder and the delay time of the delay unit are for a time period of 1024 chips.

ABSTRACT

A method and a device for multi-selection detection is disclosed in the present invention. In the said method, the length L used for the signal detection may be divided into $N_{multicoh}$ segments, then the coherent accumulating is performed; and various possible combinations will be made according to each coherent result; then those possible combinations may be coherent accumulated again; finally, the optimum ones may be selected as the detection results. The said device comprises a matched filter unit; two or more branch units; and a branch selection unit. The input signal is input to the matched filter unit for carrying out matched and filtering; the output of the matched filter unit will be sent to each branch unit respectively; the phase adjustment and the coherent accumulation of the signal will be performed in each branch unit respectively, and then sent to the branch selection unit; the branch output of selecting the largest mode is performed by the branch selection unit.

The disadvantages of the detection methods in the prior art are overcome by the method of the present invention. The influence of the frequency shift and the phase rotation that reduces the signal detection performance can be suppressed by the said method in a certain area, and the signal detection performance and the probability are improved.
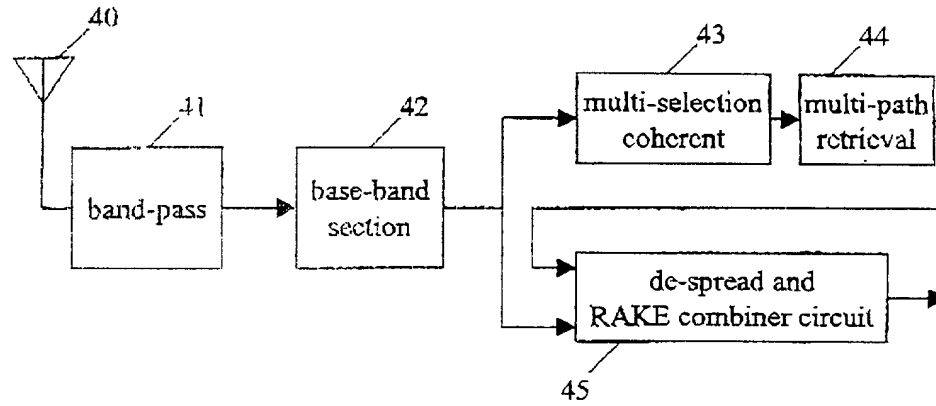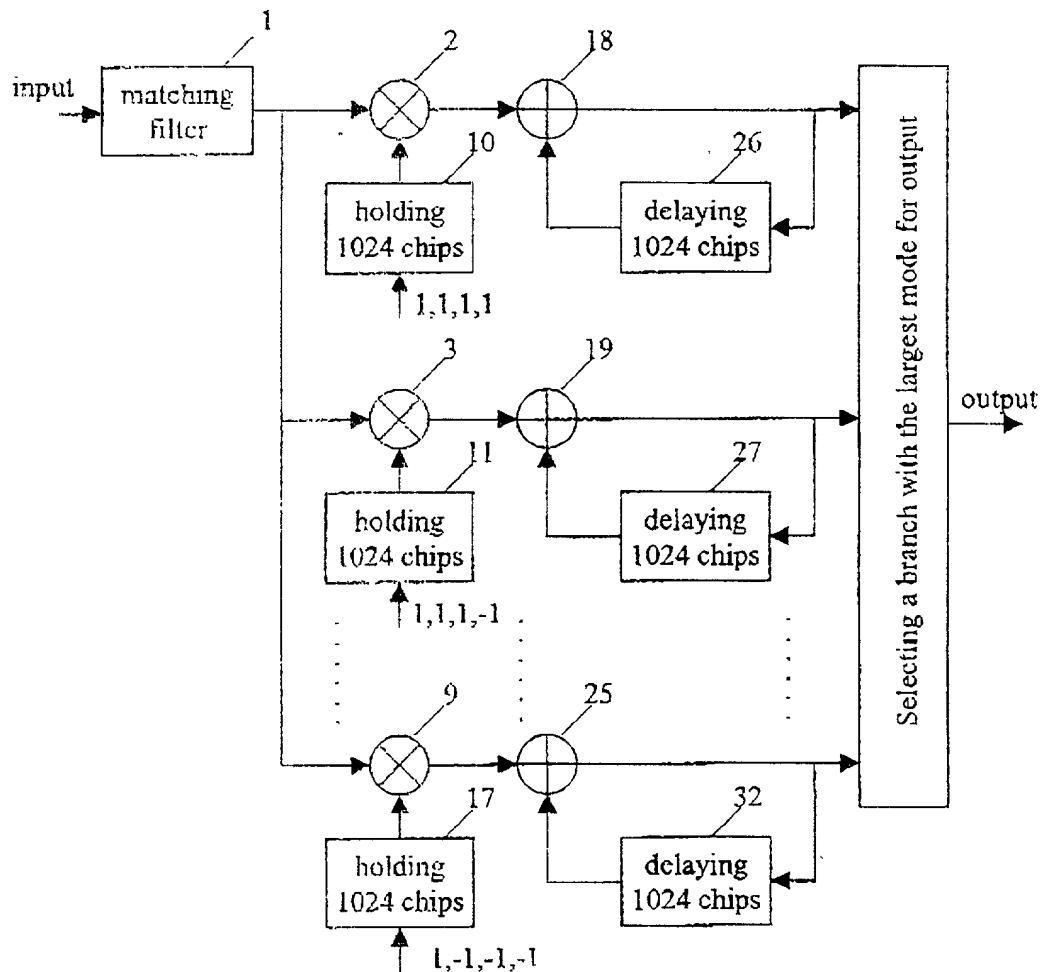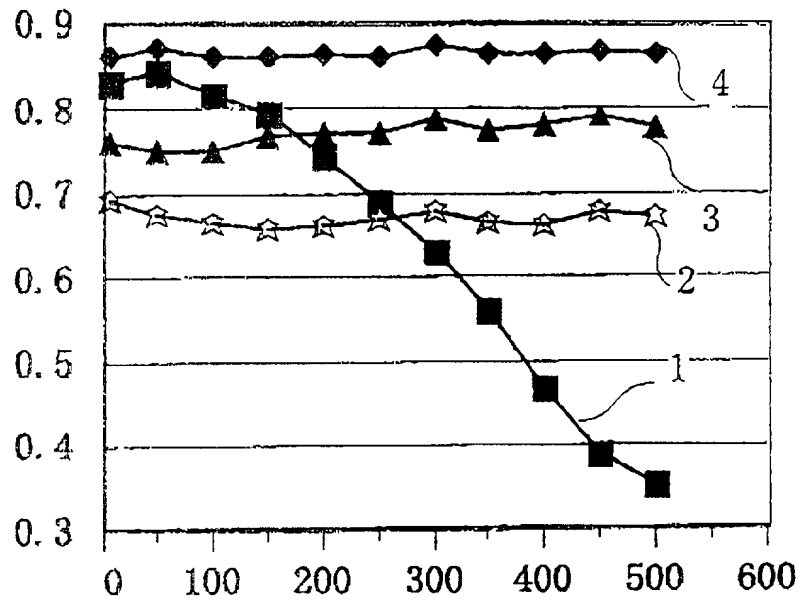
09/830385

40

band-pass
41

base-band
section
42

multi-selection
coherent
43

multi-path
retrieval
44

de-spread and
RAKE combiner circuit
45

Fig. 1

input

matching
filter
1

2

18

holding
1024 chips
10

delaying
1024 chips
26

1,1,1,1

3

19

holding
1024 chips
11

delaying
1024 chips
27

1,1,1,-1

9

25

holding
1024 chips
17

delaying
1024 chips
32

1,-1,-1,-1

Selecting a branch with the largest mode for output

output

Fig. 2

Fig. 3

MERCHANT & GOULD P.C.

United States Patent Application

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As a below named inventor I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that

I verily believe I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: A METHOD AND A DEVICE FOR MULTI-SELECTION COHERENT DETECTION

The specification of which
a. ☐ is attached hereto
b. ☒ was filed on 25 April 2001 as application serial no. 09/830385 and was amended on   (if applicable) (in the case of a PCT-filed application) described and claimed in international no. PCT/CN00/00151 filed 12 June 2000 and as amended on   (if any), which I have reviewed and for which I solicit a United States patent.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

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a. ☐ no such applications have been filed.
b. ☒ such applications have been filed as follows:

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COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	DATE OF ISSUE (day, month, year)
China	99124113.4	25 November 1999	
ALL FOREIGN APPLICATION(S), IF ANY, FILED BEFORE THE PRIORITY APPLICATION(S)			
COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	DATE OF ISSUE (day, month, year)

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U.S. APPLICATION NUMBER	DATE OF FILING (day, month, year)	STATUS (patented, pending, abandoned)

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(2)   It refutes, or is inconsistent with, a position the applicant takes in:

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(ii)   Asserting an argument of patentability.

A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability.

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